

PTO SB/08A (08-00)  
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Information Disclosure Statement by Applicant

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Sheet 1 of 1

Application Number 09/827,974

Filing Date April 6, 2001

First Named Inventor Jason Souloglou

Group Art Unit 2122

Examiner Name To Be Assigned

Attorney Docket Number 032091.0003.CIP3

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U.S. PATENT DOCUMENTS						
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		Number	Kind Code <sup>2</sup> (if known)			

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			

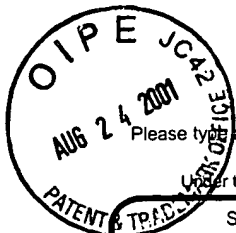
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MY		AHO, et al., "Chapter 9: Code Generation," <u>Compilers- Principles, Techniques, and Tools</u> , 522-584 (1986)	
MY		AHO, et al., "Chapter 10: Code Optimization," <u>Compilers- Principles, Techniques, and Tools</u> , 603-722 (1986)	
MY		WITCHEL, et al., "Embra: Fast and Flexible Machine Simulation," <u>Sigmetrics</u> , 68-79 (1996)	

Examiner Signature	<i>Michael Yigdal</i>	Date Considered	5/3/04
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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 1

### Complete if Known

Application Number 09/827,974  
Filing Date 4/06/01  
First Named Inventor Souloglou, J.  
Group Art Unit 2122  
Examiner Name TBA  
Attorney Docket Number 032091.0003.CIP3

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### U.S. PATENT DOCUMENTS

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MY	AA	5,768,593		Walters et al. <del>Brown, et al.</del>		

### FOREIGN PATENT DOCUMENTS

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MY	AB	Afzal, T., et al., "Motorola PowerPC Migration Tools - Emulation and Translation," Digest of Papers of the Computer society Computer Conference Compcon, US, Los Alamitos, IEEE Comp. Soc. Press, vol. Conf. 41, 25-28 February 1996 pgs. 145-150	
MY	AC	Timmins, L.J., "Techniques for Translating Object Code Into a Reduced Microprocessor Architecture," Proceedings of the ISMM International Conference. Microcomputer Applications, 5-7 Feb 1986, pgs. 181-184	
MY	AD	Andrews, K. et al., "Migrating a CISC Computer Family onto RISC Via Object Code Translation," ACM Sigplan Notices, US, Association for Computing Machinery, NY, Vol. 27, No. 9, September 1992, pgs. 213-222	
MY	AE	ADL-Tabatabai A. R., et al., "Fast Effective Code Generation in a Just-in-Time Java Compiler," ACM Sigplan Notices, US, Association for Computing Machinery, NY, Vol. 33, No. 5, May 1998, pgs. 280-290	
MY	AF	"Register Assignment Technique in Computer Emulation," IBM Technical Disclosure Bulletin.US.IBM. Corp. New York, V. 37, no. 2A, February 1994 (1994-02), pg. 157	
MY	AG	Halfhill, T.R.: "Emulation: Risc's Secret Weapon," Byte, US, McGraw-Hill, Inc. St. Peterbourough, V. 19, no. 4, April 1994 (1994-04), pgs. 119-120, 122, 124	

Examiner  
Signature

Michael Yigdal

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